

Please amend the Abstract of the Disclosure at page 52 as follows:

SYSTEM AND METHOD FOR HANDLING UPDATES TO MEMORY IN A
DISTRIBUTED SHARED MEMORY SYSTEM

ABSTRACT OF THE DISCLOSURE

A processor (100) in a distributed shared memory computer system (10) receives ownership of data and initiates an initial update to memory request ~~to a front side bus processor interface (24).~~ The front A front side bus processor interface (24) forwards the initial update to memory request to a memory directory interface unit (22). ~~As the memory directory interface unit (22) processes the initial update to memory request, the front~~ The front side processor interface (24) may receive subsequent update to memory requests for the data from ~~the processor (100) or other processors (101)~~ co-located on the same local bus. Front side bus processor interface (24) maintains a most recent subsequent update to memory in a queue (102). Once the data has been updated in its home memory (17), the memory directory interface unit (22) sends a writeback acknowledge to the front side bus processor interface (24). The most recent subsequent update to memory request in the queue (102) is then forwarded by the front side bus processor interface (24) to the memory directory interface unit (24) for processing.



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ABSTRACT OF THE DISCLOSURE

A processor (100) in a distributed shared memory computer system (10) receives ownership of data and initiates an initial update to memory request. A front side bus processor interface (24) forwards the initial update to memory request to a memory directory interface unit (22). The front side processor interface (24) may receive subsequent update to memory requests for the data from processors co-located on the same local bus. Front side bus processor interface (24) maintains a most recent subsequent update to memory in a queue (102). Once the data has been updated in its home memory (17), the memory directory interface unit (22) sends a writeback acknowledge to the front side bus processor interface (24). The most recent subsequent update to memory request in the queue (102) is then forwarded by the front side bus processor interface (24) to the memory directory interface unit (24) for processing.